

Fig. 1

Block diagram of a plucking circuit 22. The circuit includes a Plucking Enable Circuit 30, a Reference Voltage and Plucking Pulse Generator 34, and a Counter Circuit 38. HF1 input goes to 30 and 34. EN-PLUCK output of 30 goes to 34. 34 outputs PLUCK and a signal to 38. 38 outputs Q0 and Q1, which are connected to EOB and HFCLK outputs respectively. A feedback loop goes from EOB back to 30 and 34.

Fig. 2

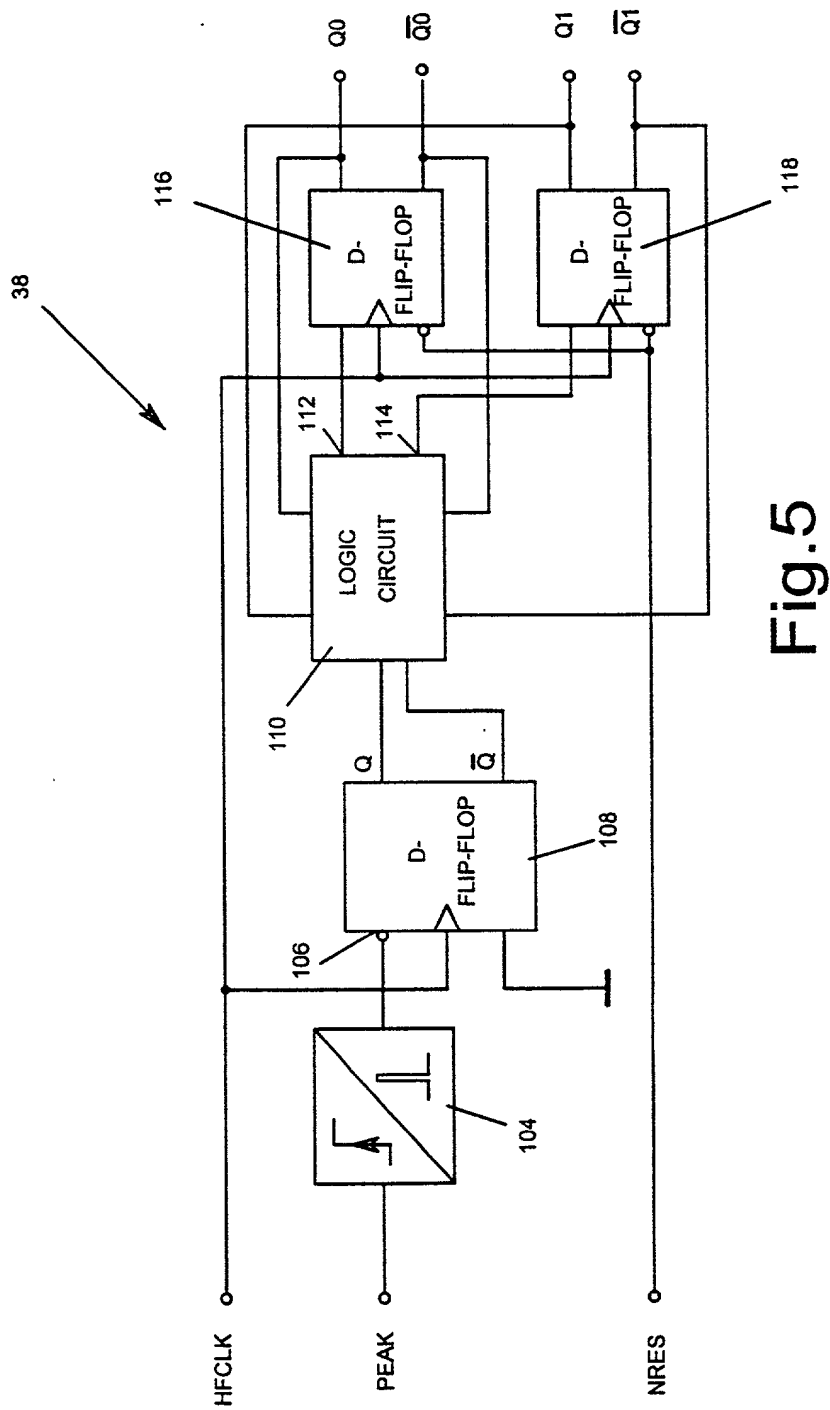


Fig.5

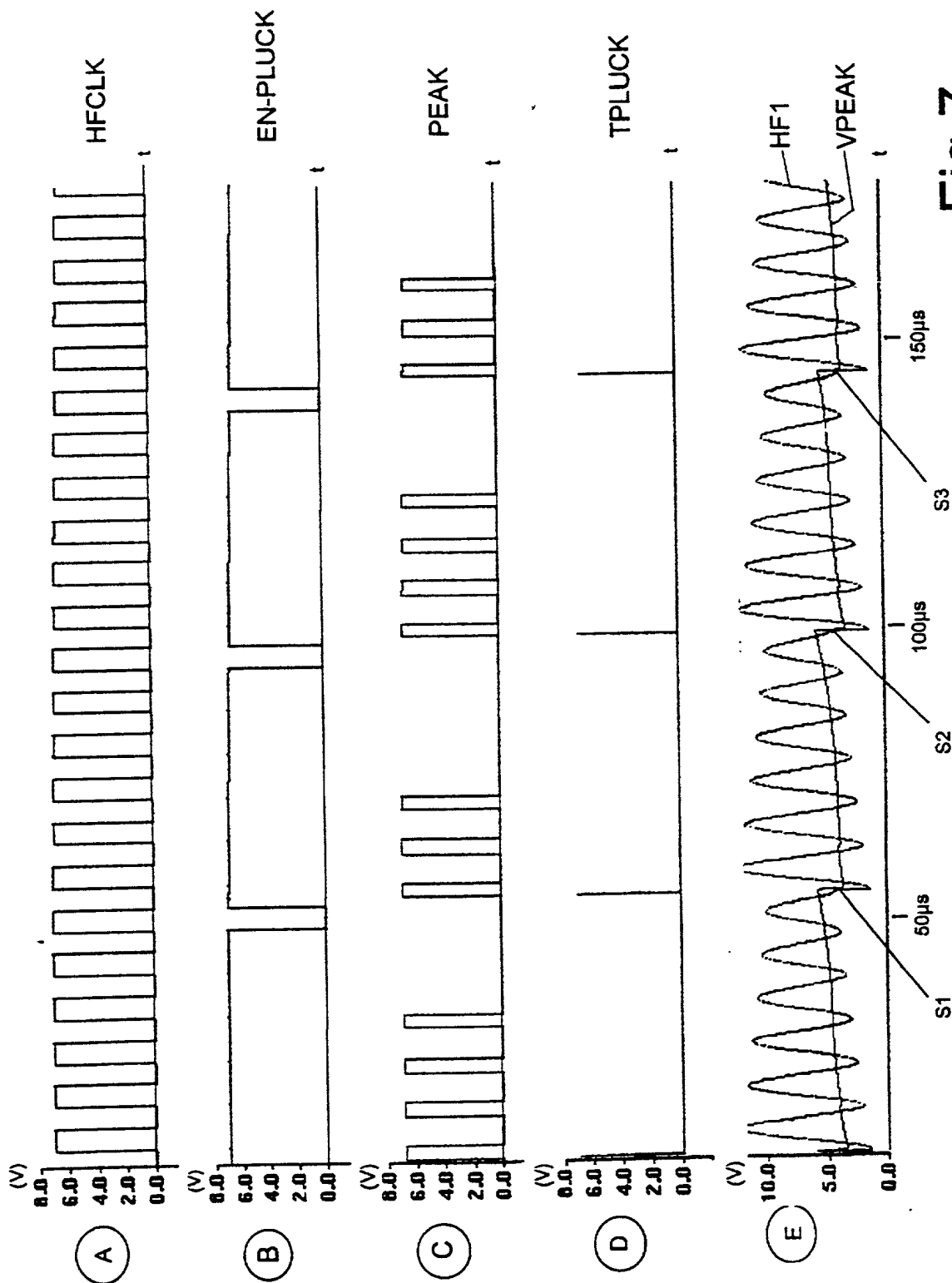


Fig.7